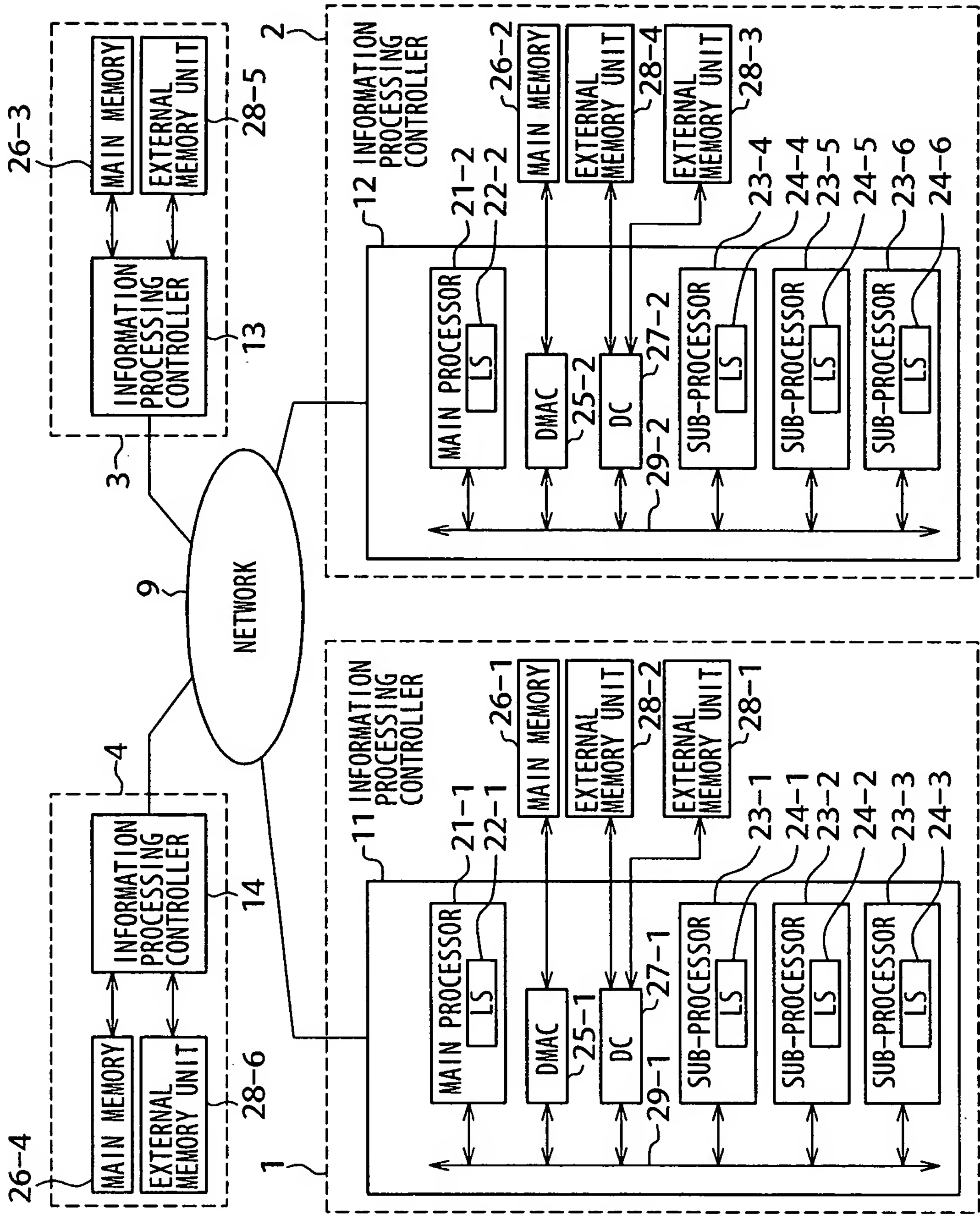


FIG. 1



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FIG. 2A

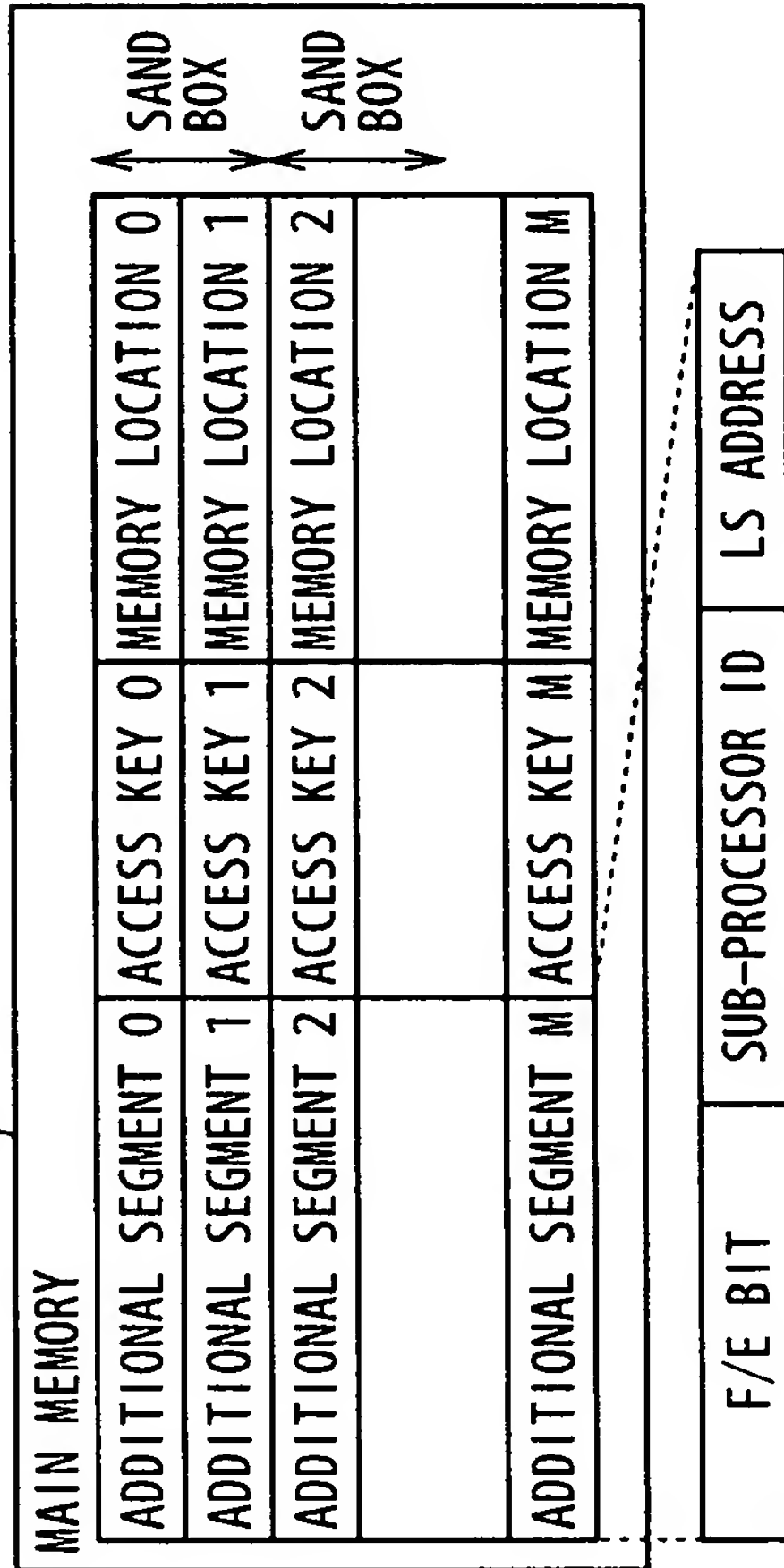


FIG. 2C

SUB-PROCESSOR ID			
0	SUB-PROCESSOR KEY 0	KEY MASK 0	
1	SUB-PROCESSOR KEY 1	KEY MASK 1	
2	SUB-PROCESSOR KEY 2	KEY MASK 2	
N	SUB-PROCESSOR KEY N	KEY MASK N	

FIG. 2B

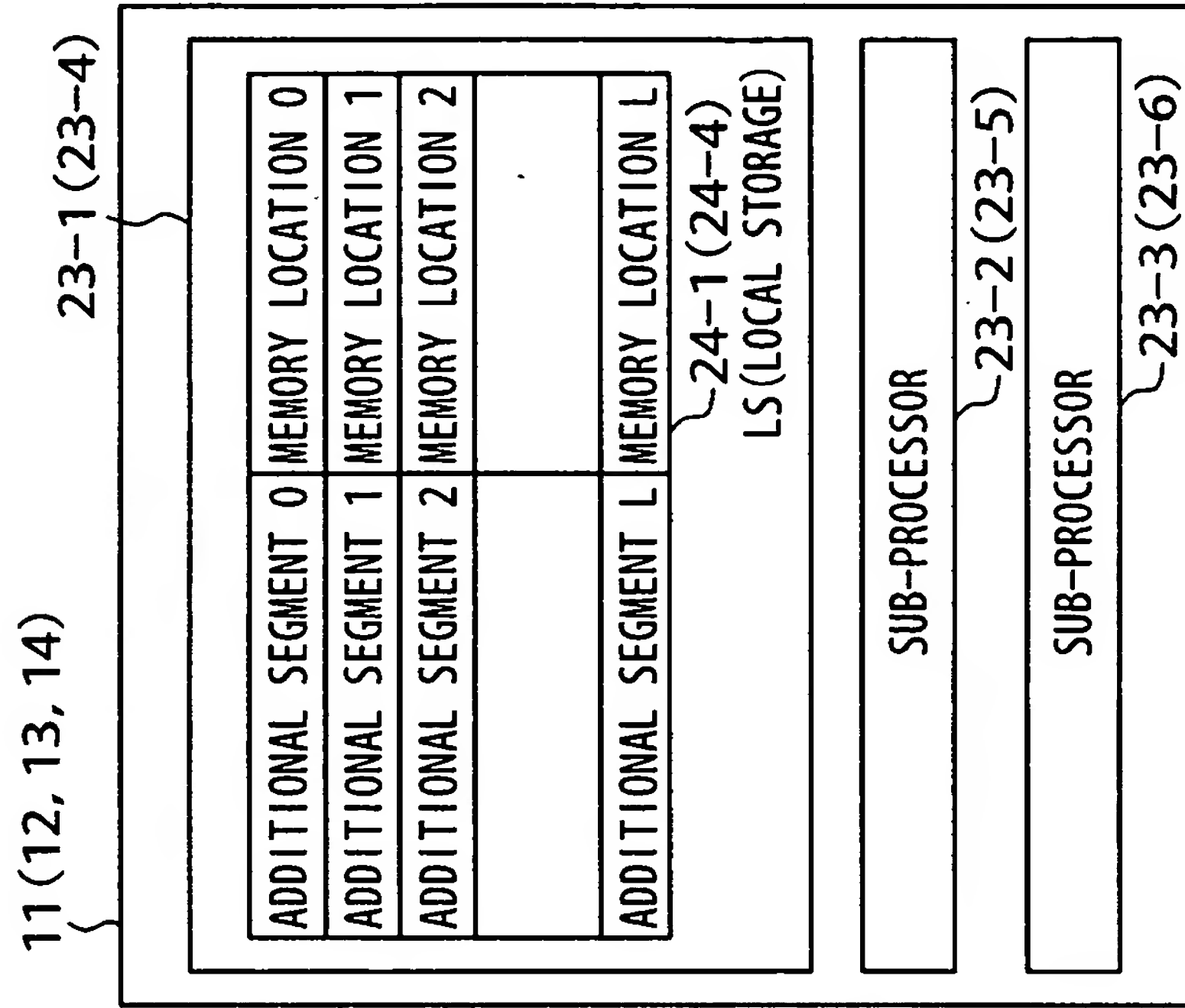


FIG. 3

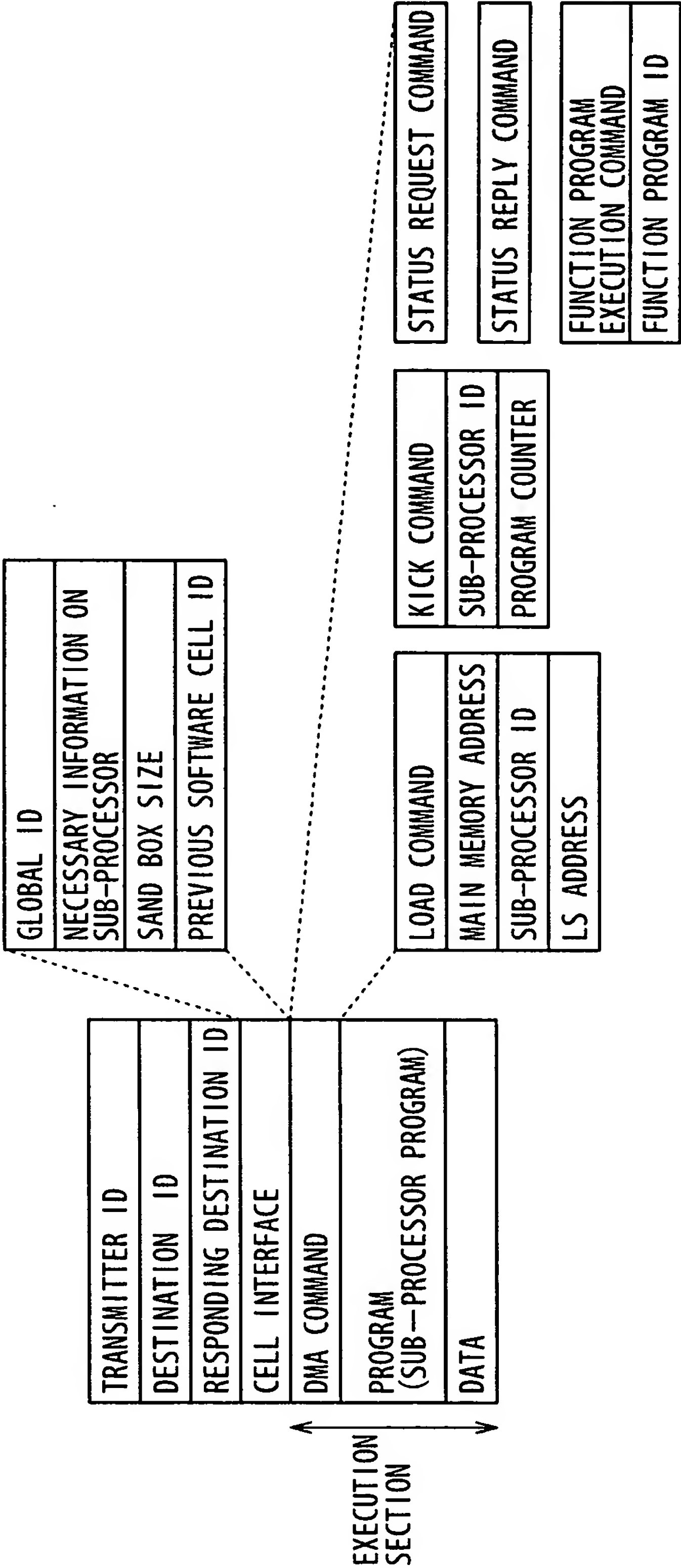


FIG. 4

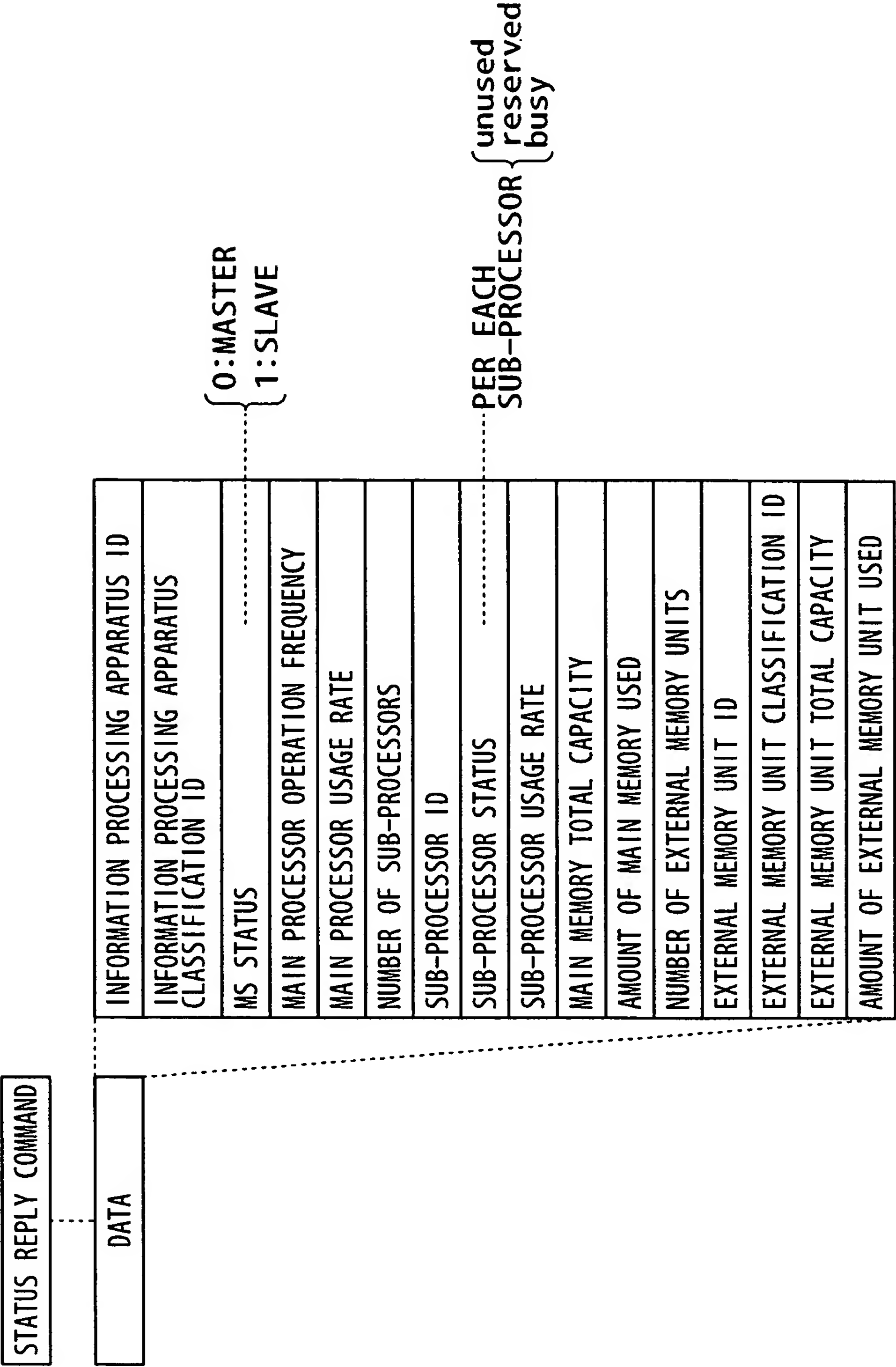


FIG. 5

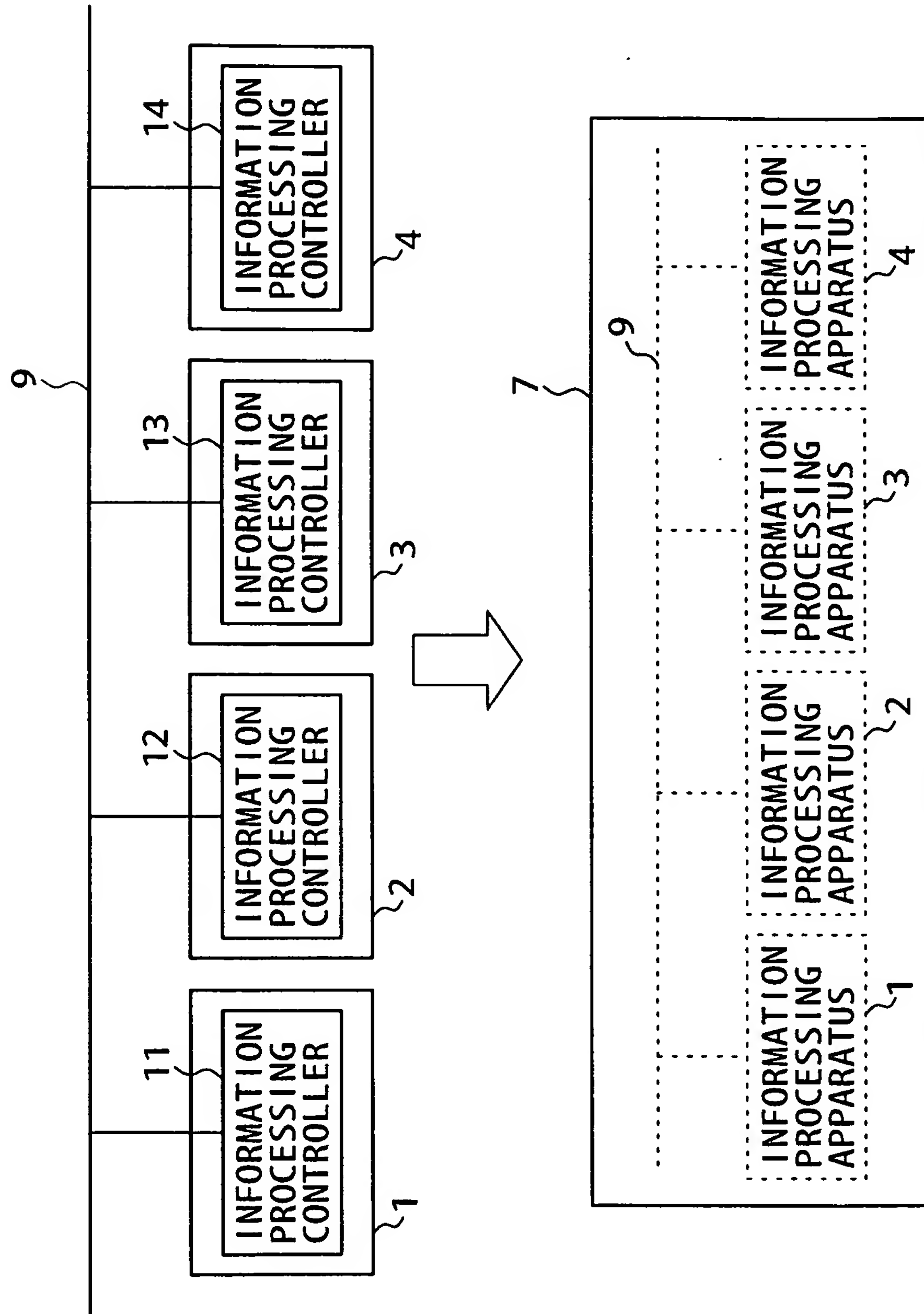
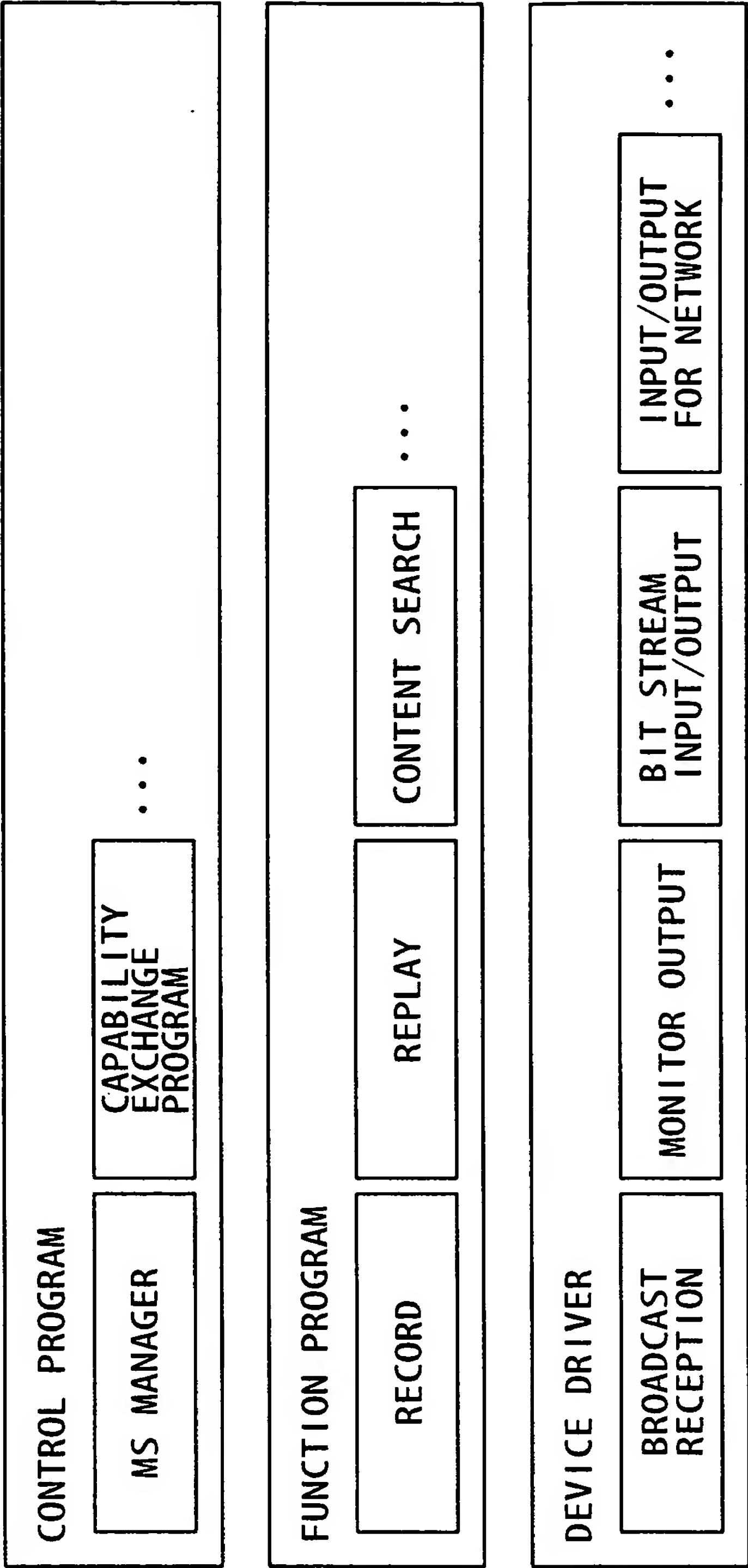


FIG. 6



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FIG. 7

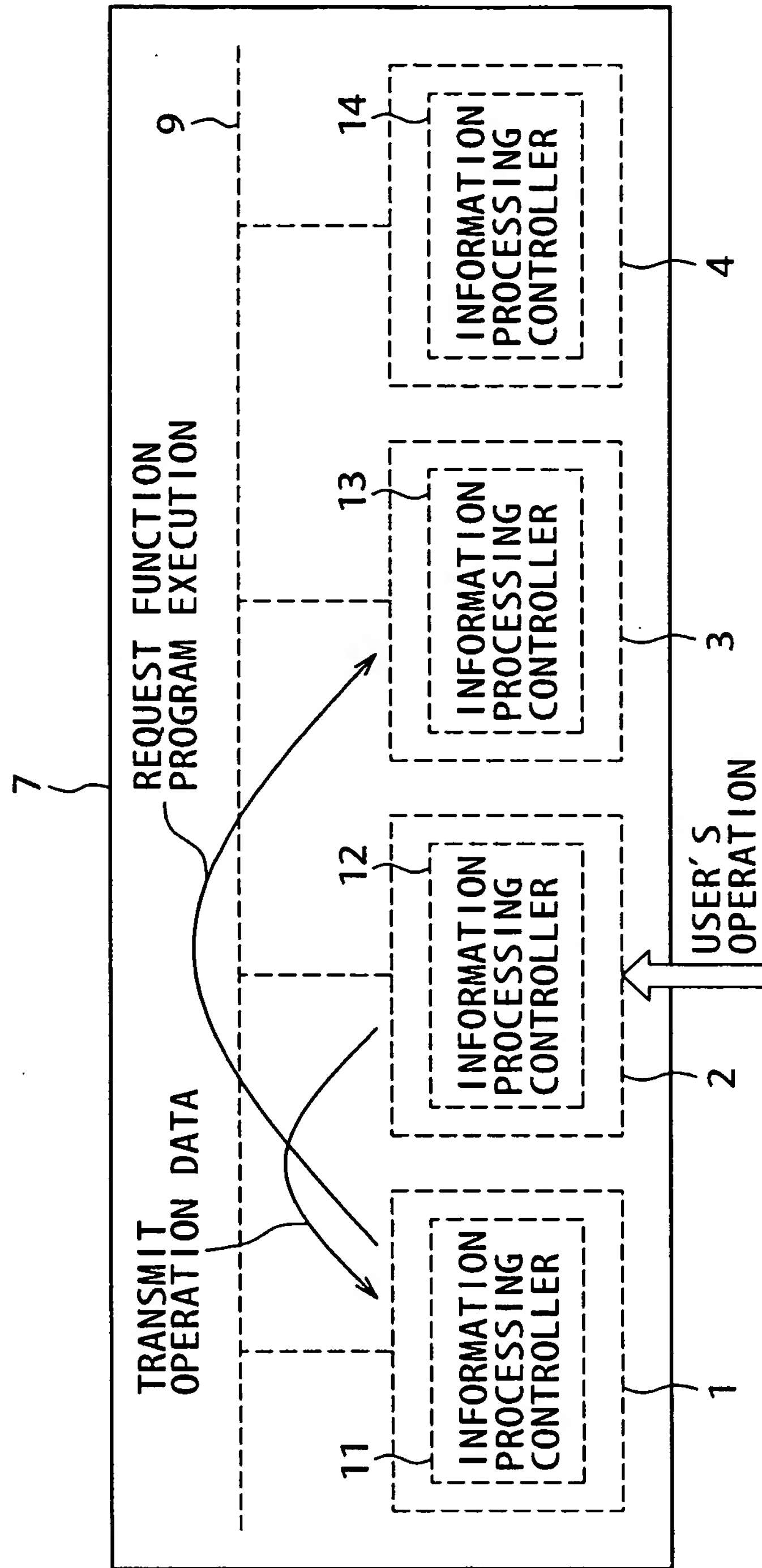
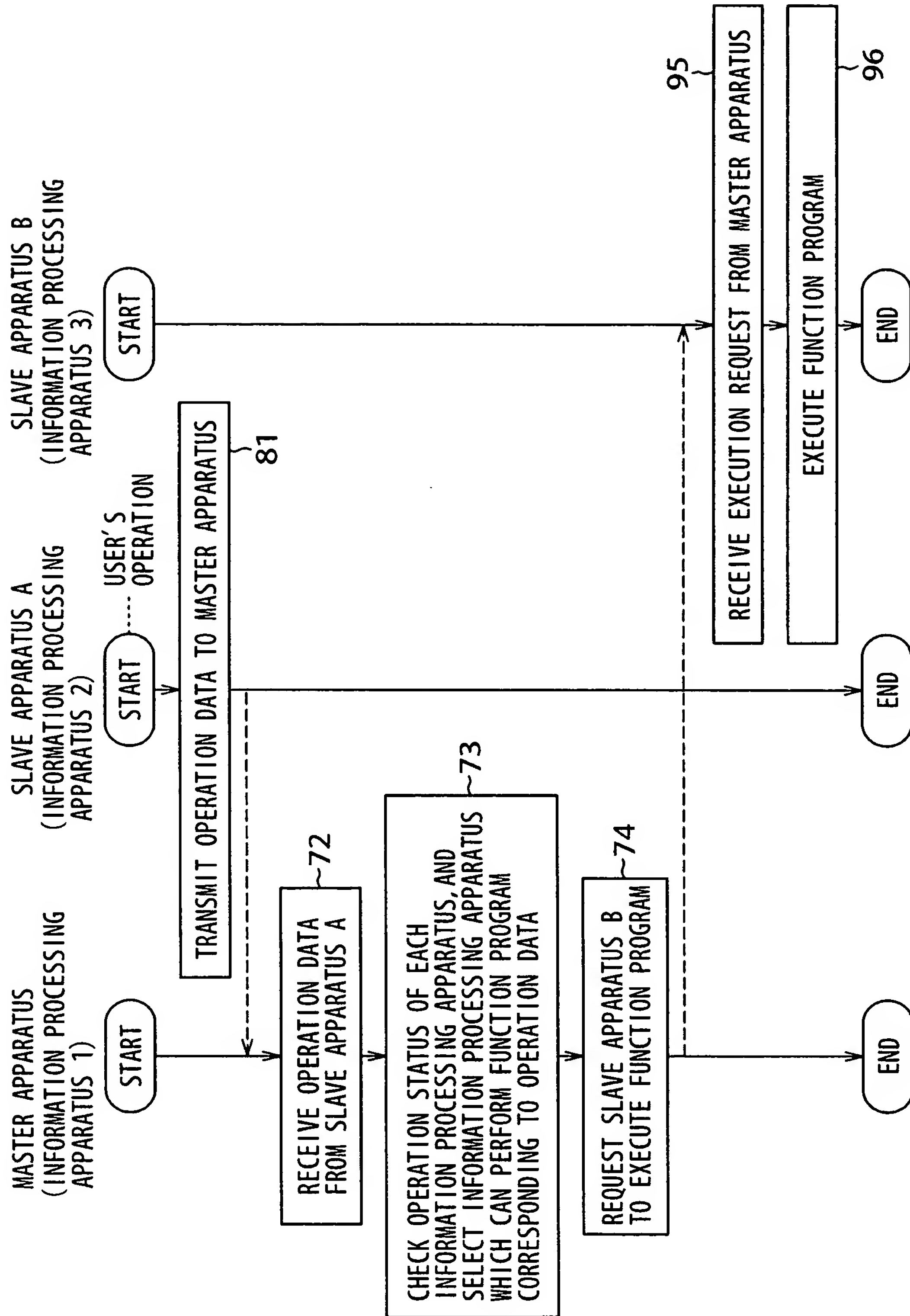
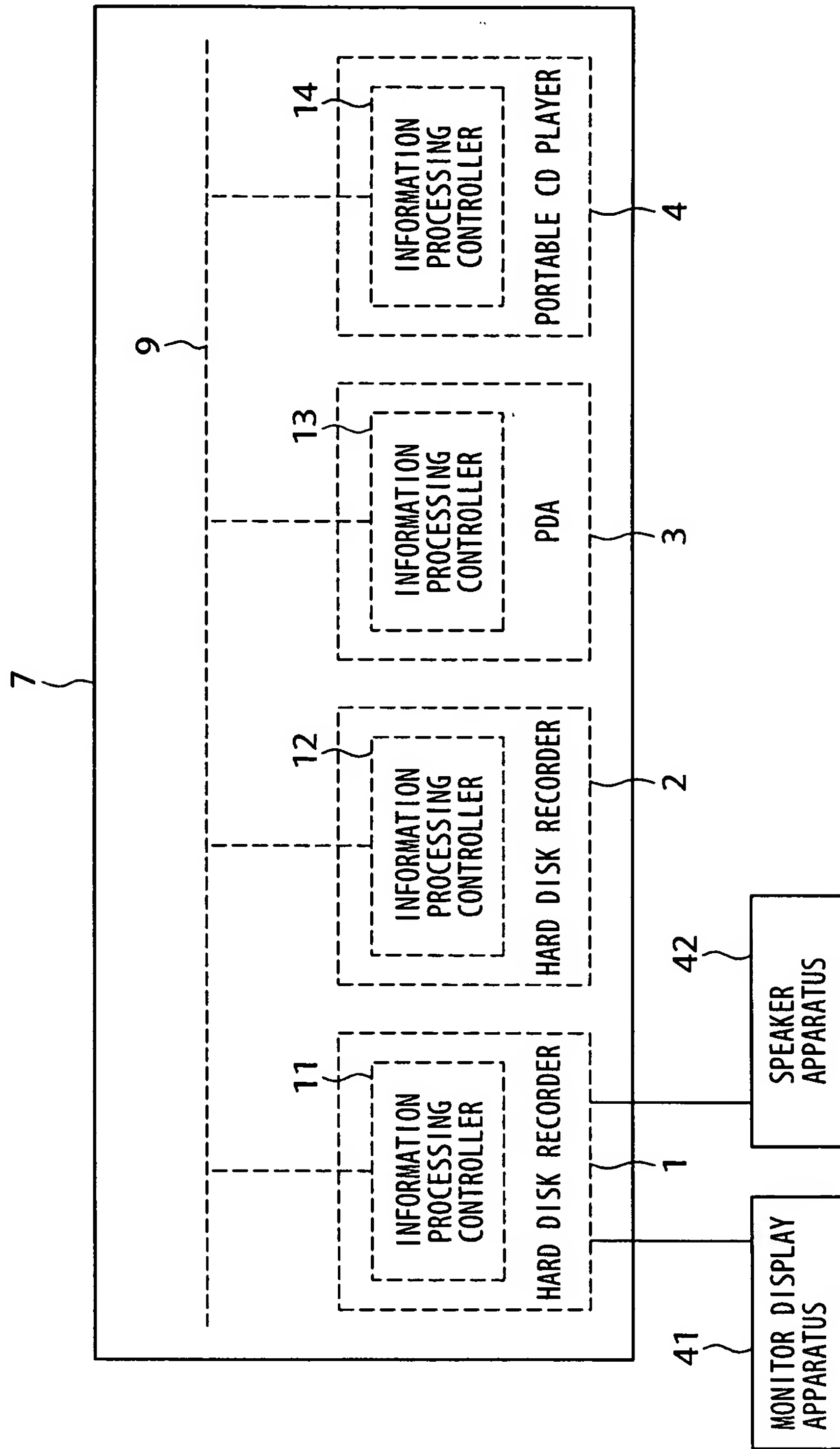


FIG. 8



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FIG. 9



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FIG. 10

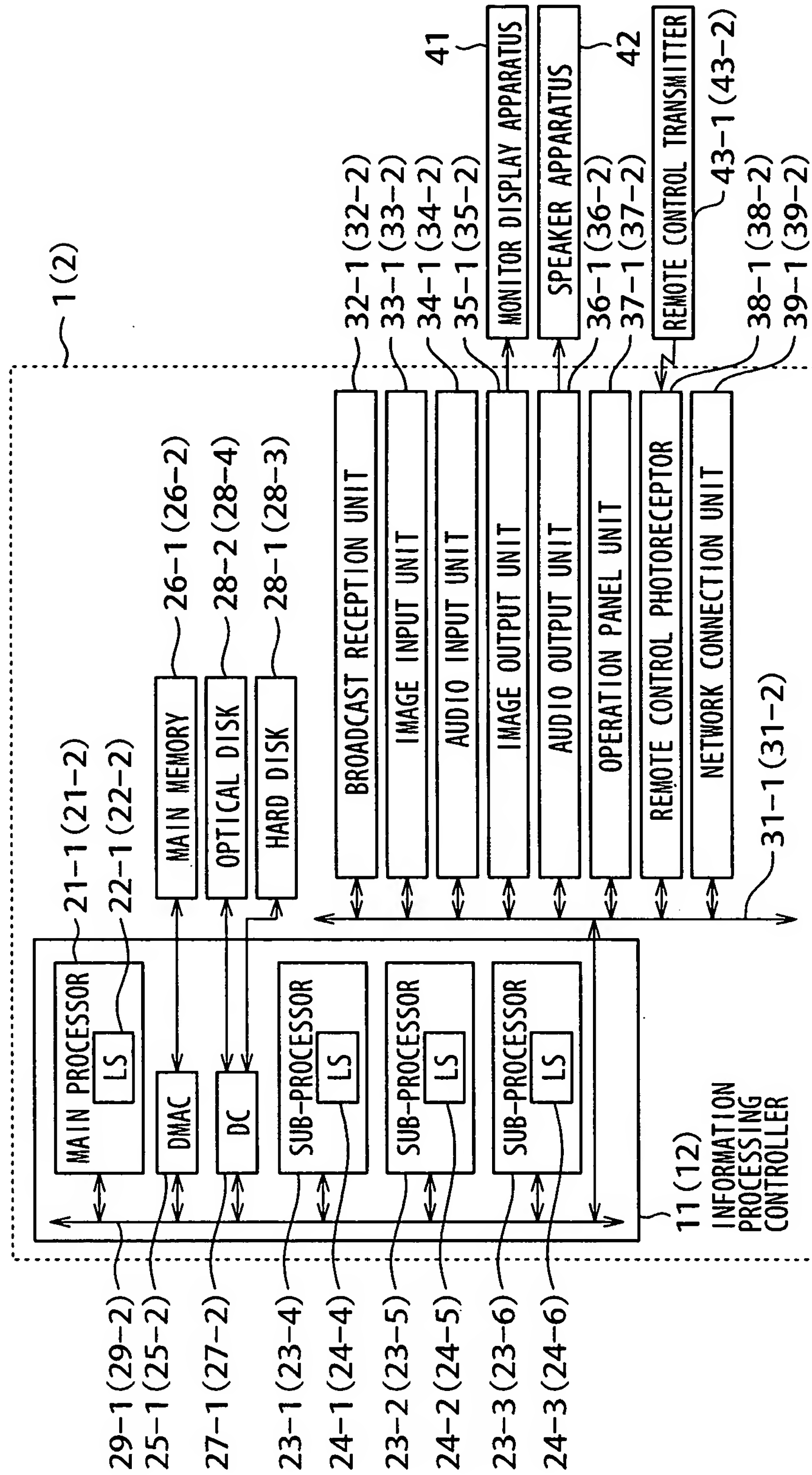


FIG. 11

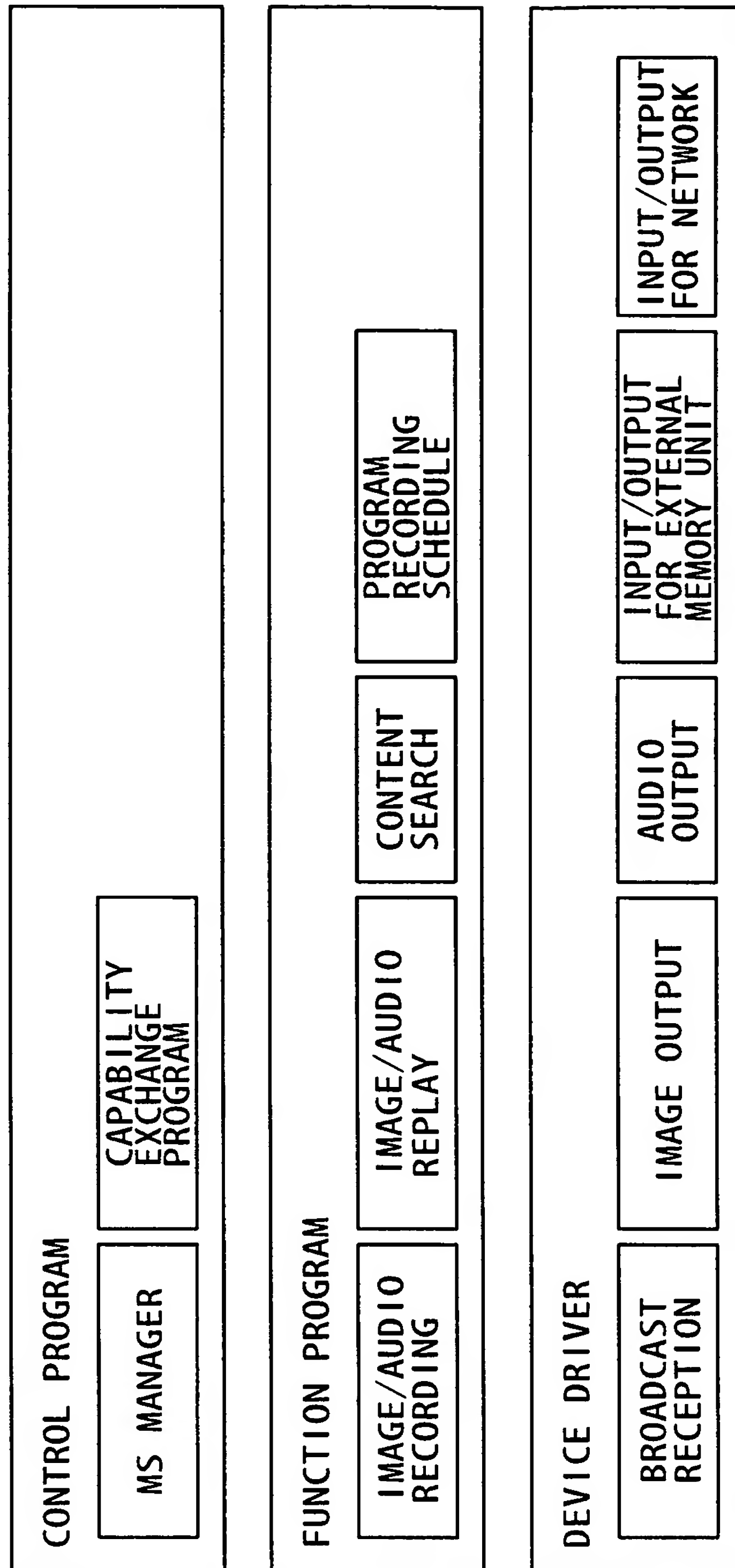


FIG. 12

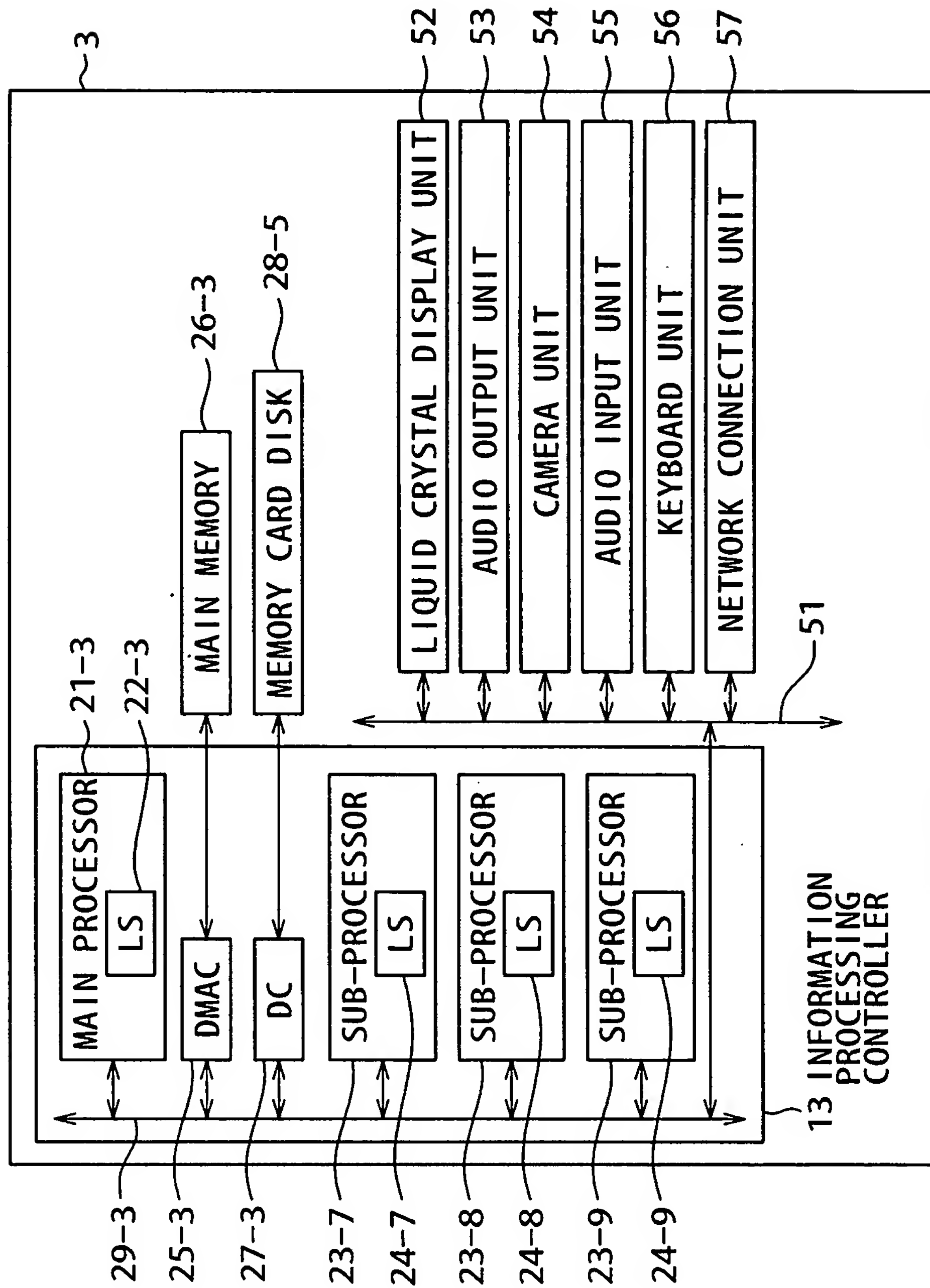


FIG. 13

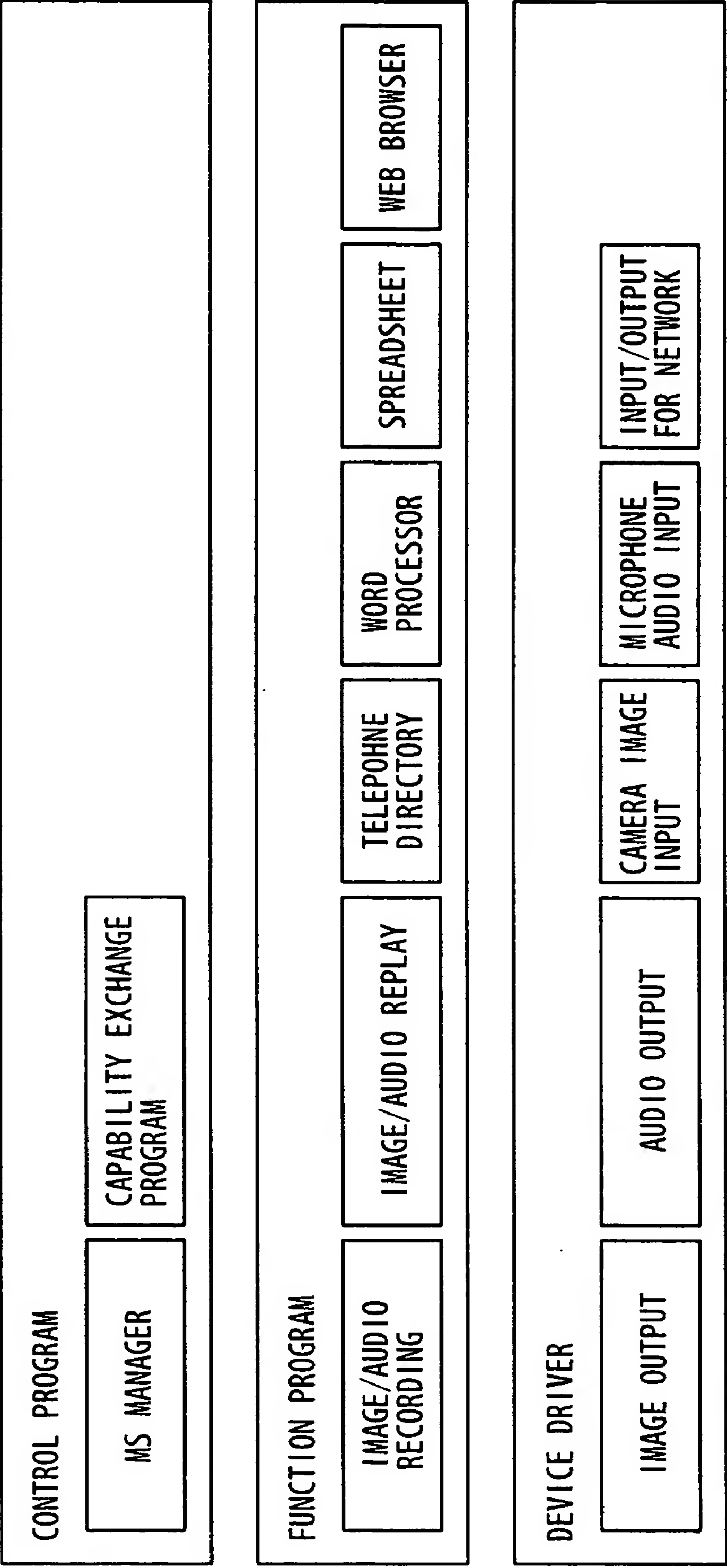


FIG. 14

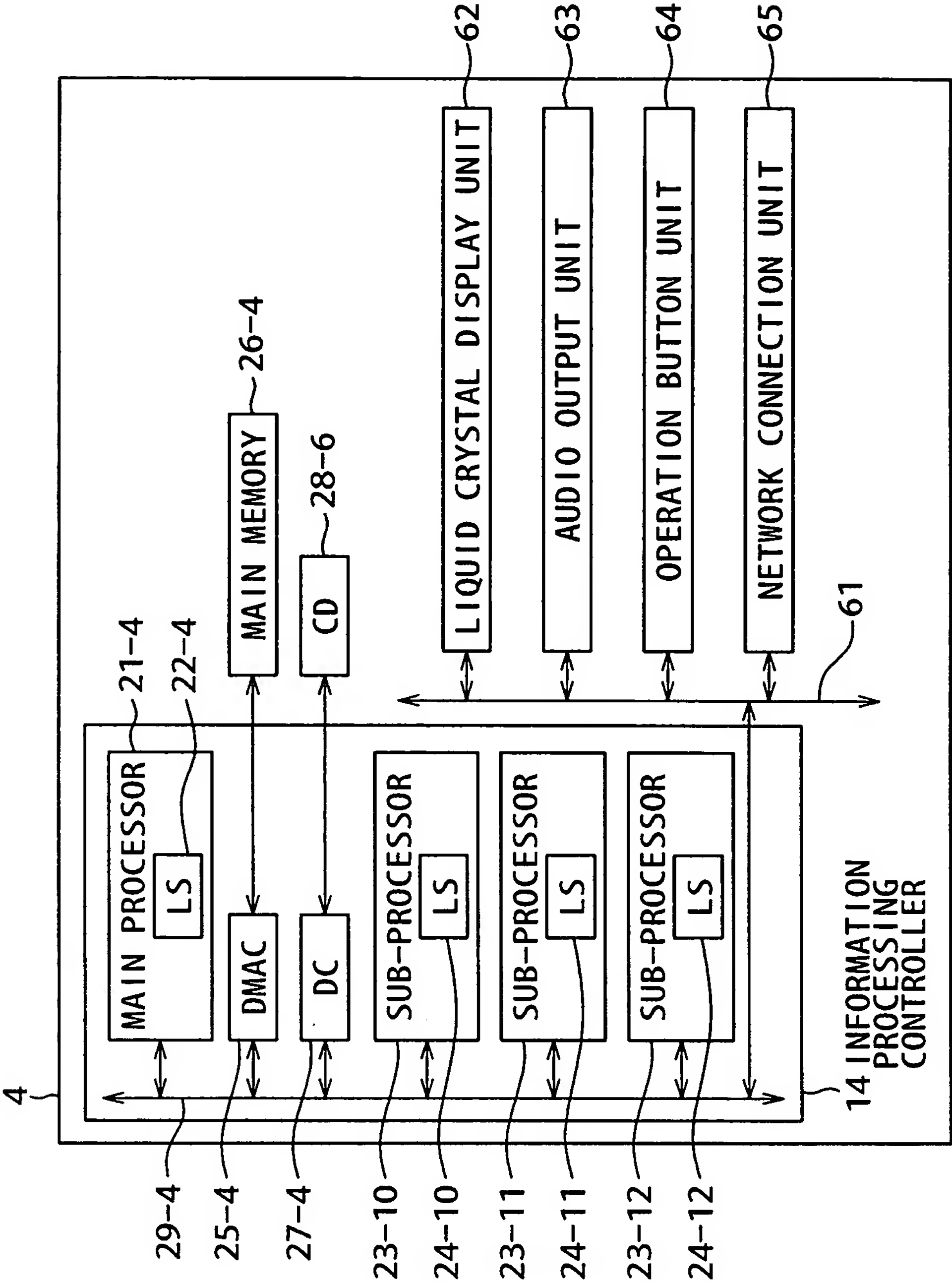


FIG. 15

